

University of Pretoria Yearbook 2025

Analogue electronic design 732 (EME 732)

Qualification	Postgraduate
Faculty	Faculty of Engineering, Built Environment and Information Technology
Module credits	32.00
NQF Level	08
Programmes	BEngHons specialising in Electronic Engineering
Prerequisites	EME 732 (E5), 3rd year Electronics or equivalent or permission from the lecturer
Contact time	32 contact hours per semester
Language of tuition	Module is presented in English
Department	Electrical, Electronic and Computer Engineering
Period of presentation	Semester 1

Module content

The integrated circuit (IC) or “chip” is the motor of the present electronic revolution. The ever-increasing impact of electronics is driven mainly by large-scale ICs such as processor and memory chips. The electronic circuit techniques used in these chips can only be understood on a deep level by a study of classical analogue electronics aimed at integrated circuit design for fabrication in CMOS, bipolar and BiCMOS processes. In addition, analog circuit techniques perform an essential role in the interfaces between the “real world” and digital systems. Examples are: voltage references, amplifiers, filters, level-converters, buffers. Important topics in this respect are feedback and stability theory as specialized for electronic circuits. The course includes: IC fabrication technology, models for IC transistors, transistor current sources and amplifiers, output stages, operational amplifiers, frequency response and stability of feedback amplifiers, nonlinear and computational circuits.

General Academic Regulations and Student Rules

The [General Academic Regulations \(G Regulations\)](#) and [General Student Rules](#) apply to all faculties and registered students of the University, as well as all prospective students who have accepted an offer of a place at the University of Pretoria. On registering for a programme, the student bears the responsibility of ensuring that they familiarise themselves with the General Academic Regulations applicable to their registration, as well as the relevant faculty-specific and programme-specific regulations and information as stipulated in the relevant yearbook. Ignorance concerning these regulations will not be accepted as an excuse for any transgression, or basis for an exception to any of the aforementioned regulations. The G Regulations are updated annually and may be amended after the publication of this information.

Regulations, degree requirements and information

The faculty regulations, information on and requirements for the degrees published here are subject to change

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University of Pretoria Programme Qualification Mix (PQM) verification project

The higher education sector has undergone an extensive alignment to the Higher Education Qualification Sub-Framework (HEQSF) across all institutions in South Africa. In order to comply with the HEQSF, all institutions are legally required to participate in a national initiative led by regulatory bodies such as the Department of Higher Education and Training (DHET), the Council on Higher Education (CHE), and the South African Qualifications Authority (SAQA). The University of Pretoria is presently engaged in an ongoing effort to align its qualifications and programmes with the HEQSF criteria. Current and prospective students should take note that changes to UP qualification and programme names, may occur as a result of the HEQSF initiative. Students are advised to contact their faculties if they have any questions.